

**AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior version, and listings, of claims in the application:

**Listing of Claims:**

1. **(currently amended)** An electronic apparatus ~~suitable~~ for displaying information via a display device, the display device having a display panel provided with driving electronics, the electronic apparatus comprising:

a controller for selecting at least one application for the display device; ~~and further comprising memory means~~

a memory for storing at least display parameters related to said application and ~~means~~ for providing said display parameters to an interface between the electronic apparatus and the display device, the display parameters comprising at least one belonging to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and power saving parameters for the display device; ~~and~~

a processor for sending a sequence of data blocks to the controller during at least one dummy line of a frame, wherein no image data is to be sent to the display device during said at least one dummy line, wherein one of the data blocks is a predetermined bit pattern to be recognized by the controller, and wherein the display parameters are provided in the remaining data blocks of the sequence;

wherein if the controller does not recognize the predetermined bit pattern, the display parameters provided in the remaining data blocks of the sequence are ignored; and

wherein if the controller recognizes the predetermined bit pattern, the display parameters are loaded from the remaining data blocks of the sequence into the memory.

2. **(currently amended)** The electronic apparatus of claim 1, said display parameters further comprising ~~memory means for storing~~ parameters related to the selection of driving transistors.

3-5. (Canceled)

6. (currently amended) A display device, ~~for use in an electronic apparatus comprising:~~   
~~a display panel provided with driving electronics;~~  
a controller for selecting at least one application for the display device; ~~and further comprising~~

~~a memory means for storing at least display parameters related to said application and means for providing said display parameters to the driving electronics an interface between the electronic apparatus and the display device, the display parameters comprising at least one belonging to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and power saving parameters for the display device; the display device comprising:~~

~~a display panel provided with driving electronics; and~~

~~means for recognizing an identification code at an interface between the electronic apparatus and the display device~~

~~a processor for sending a sequence of data blocks to the controller during at least one dummy line of a frame, wherein no image data is to be sent to the display panel during said at least one dummy line, wherein one of the data blocks is a predetermined bit pattern to be recognized by the controller, and wherein the display parameters are provided in the remaining data blocks of the sequence;~~

~~wherein if the controller does not recognize the predetermined bit pattern, the display parameters provided in the remaining data blocks of the sequence are ignored; and~~

~~wherein if the controller recognizes the predetermined bit pattern, the display parameters are loaded from the remaining data blocks of the sequence into the memory.~~

7. (currently amended) A display device according to claim 6, the driving electronics further comprising ~~a storage means for storing the display a sequence of~~ parameters controlling the ~~display panel received via the interface from the memory electronic apparatus.~~

8. (currently amended) The electronic apparatus of claim 1, wherein the display parameters further comprise include at least one of: a gate select width, a gate enable width, and a power saving pulse width.

9. (currently amended) A method of an electronic apparatus controlling a display device for at least one application, the method comprising:

programming into a memory of the electronic apparatus display parameters related to the application, the display parameters comprising including at least one selected from a group ~~consisting~~ of: number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and power saving parameters for the display device; [[and]]

providing the display parameters from the electronic apparatus to the display device;

sending a sequence of data blocks to a controller of the display device during at least one dummy line of a frame, wherein no image data is to be sent to the display device during said at least one dummy line, wherein one of the data block is a predetermined bit pattern to be recognized by the controller of the display device, and wherein the display parameters are provided in the remaining data blocks of the sequence;

if the controller of the display device does not recognize the predetermined bit pattern, ignoring the display parameters provided in the remaining data blocks of the sequence;

if the controller of the display device recognizes the predetermined bit pattern, loading the display parameters from the remaining data blocks of the sequence into a memory of the display device.

10. (previously presented) The method of claim 9 , further comprising storing the display parameters in a memory of the display device.

11. (currently amended) The method of claim 9, wherein the display parameters further comprise include at least one of ~~the parameters~~: a gate select width; a gate enable width; and a power saving pulse width.

12. (previously presented) The electronic apparatus of claim 1, wherein the controller is adapted to select the one application from a group of applications including both a telephone application and a calculator application.

13. (previously presented) The method of claim 9, wherein the application is one of a telephone application and a calculator application.

14. (canceled)

15. (**currently amended**) The method of claim [[14]] 9, wherein said at least one dummy line includes the first line of said frame.

16. (**currently amended**) The method of claim [[14]] 9, wherein said predetermined bit pattern is in the first data block of said sequence.

17. (**currently amended**) The method of claim [[14]] 9, wherein the display parameters are provided during a plurality of dummy lines of said frame.

18. (**currently amended**) The method of claim [[14]] 9, wherein at least one data block in said sequence defines at least one of  
the number of dummy lines at the beginning of each frame; and  
the number of dummy pixels inserted at the beginning of each line before the image data of each frame.